

<b>Notice of References Cited</b>	Application/Control No. 10/086,267	Applicant(s)/Patent Under Reexamination AGAZZI, OSCAR E.	
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#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,968,988	11-1990	Miki et al.	341/141
	B	US-5,239,299 A	08-1993	Apple et al.	341/118
	C	US-5,294,926 A	03-1994	Corcoran, John J.	341/120
	D	US-6,009,120 A	12-1999	Nobakht, Ramin A.	375/229
	E	US-6,081,215 A	06-2000	Kost et al.	341/120
	F	US-6,363,112 B1	03-2002	Azadet et al.	375/233
	G	US-6,377,640 B2	04-2002	Trans, Francois	375/354
	H	US-6,438,163 B1	08-2002	Raghavan et al.	375/233
	I	US-6,621,862 B1	09-2003	Dabell, Steve	375/229
	J	US-6,678,319 B1	01-2004	Jamali, Hamadi	375/234
	K	US-6,731,683	05-2004	Fiedler et al.	375/232
	L	US-6,795,494 B1	09-2004	Phanse et al.	375/219
	M	US-6,859,508 B1	02-2005	Koyama et al.	375/348

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	High-speed VLSI implementation of reduced complexity sequence estimation algorithms with application to Gigabit Ethernet 1000Base-T; Haratsch, E.F.; VLSI Technology, Systems, and Applications, 1999. Int'l Symposium on 8-10 June 1999 Page(s):171 - 174
	V	A 1-Gb/s joint equalizer and trellis decoder for 1000BASE-T Gigabit Ethernet; Azadet, K. et al; Solid-State Circuits, IEEE Journal of Volume 36, Issue 3, March 2001 Page(s):374 - 384
	W	Low-power equalizer architectures for high speed modems Azadet, K. et al; Communications Magazine, IEEE Volume 36, Issue 10, Oct. 1998 Page(s):118 - 126
	X	A gigabit transceiver chip set for UTP CAT-6 cables in digital CMOS technology Azadet, K. et al; Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000 IEEE International 7-9 Feb. 2000 Page(s):306 - 307

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,934,387 B1	08-2005	Kim, Beomsup	379/406.08
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gigabit Ethernet over unshielded twisted pair cables Azadet, K.; VLSI Technology, Systems, and Applications, 1999. International Symposium on 8-10 June 1999 Page(s):167 - 170
	V	A DSP based receiver for 1000BASE-T PHY; Runsheng He; Nazari, N.; Sutardja, S.; Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 2001 IEEE International 5-7 Feb. 2001 Page(s):308 - 309, 458
	W	A CMOS transceiver analog front-end for gigabit ethernet over CAT-5 cables Roo, P. et al; Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 2001 IEEE International 5-7 Feb. 2001 Page(s):310 - 311, 458
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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